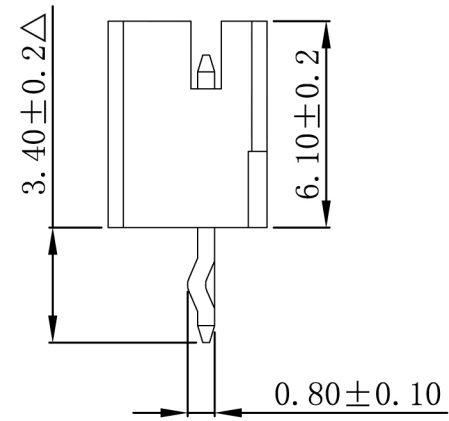
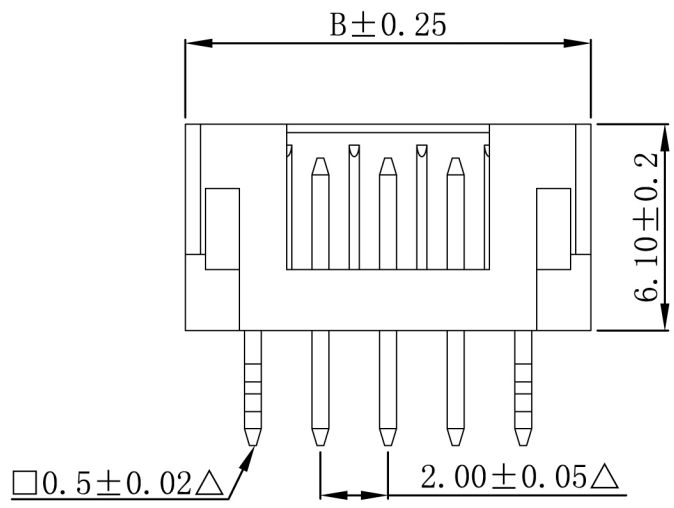


P.C.B. Hole Layout

Specificatis

- Current Rating : 3A AC, DC
- Voltage rating : 300V AC, DC
- Temperature range : -25°C ~ +85°C
- Contact resistance : 20mΩ Max
- Insulation resistance : 100MΩ Min
- Withstanding voltage : 1000 V AC/minute
- Pin Material : Brass
- Finished : Tin-plated
- Housing Material : Nylon-66, UL94 V-0
- Color : reddish orange



PARTS NO.	Circuits	Dimension mm	
		A (mm)	B (mm)
CAE-W20-02	2	2.0	6.0
CAE-W20-03	3	4.0	8.0
CAE-W20-04	4	6.0	10.0
CAE-W20-05	5	8.0	12.0
CAE-W20-06	6	10.0	14.0
CAE-W20-07	7	12.0	16.0
CAE-W20-08	8	14.0	18.0
CAE-W20-09	9	16.0	20.0
CAE-W20-10	10	18.0	22.0
CAE-W20-11	11	20.0	24.0
CAE-W20-12	12	22.0	26.0
CAE-W20-13	13	24.0	28.0
CAE-W20-14	14	26.0	30.0
CAE-W20-15	15	28.0	32.0
CAE-W20-16	16	30.0	34.0

DESCRIPTION(內容): CAE2.0mm Wafer 180° □=0.50 打K (環保材料) PART NO: (料 號) CAE-W20-XX DWG NO(圖號):	DRAWN(製圖): 方婷	GENERAL TOLERANCE: (公差) .0 ±0.20 .00 ±0.15 .000 ±0.10	REV: A/2	REV 1	DESCRIPTION △此符號表示重點管控尺寸	DATE 2009.06.30	
	CHECKED(確認):	APPROVAL(核准):	SCALE(比例): 1:XX	UNIT: mm	2 更正顏色英文字母 (橘紅色)	2010.11.02	
				SIZE: A4	3		
				SHEET: 1/1	4		